



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tatsuya Usami

Serial No.: 09/851,313

Group Art Unit: 2823

Filed: May 9, 2001

Examiner: Maldonado, Julio J.

For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR WAFER, AND
METHODS OF PRODUCING THE SAME DEVICE AND WAFER

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

**RESPONSE TO FIRST OFFICE ACTION AND
RESTRICTION REQUIREMENT**

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TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action dated March 22, 2002, please consider the following:

REMARKS

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group I (e.g., drawn to a semiconductor device as defined by claims 1-8), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

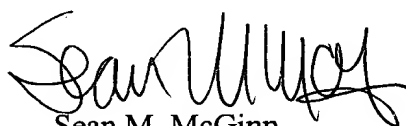
Further, Applicant respectfully requests the Examiner's acknowledgment of the priority document filed on May 9, 2001.

Early, favorable prosecution on the merits is respectfully requested.

Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.

A conditional petition is made for any extension of time which may become necessary. The Commissioner is authorized to charge any fees for such extension and to credit any overpayment in fees to Attorney's Deposit Account No. 50-0481.

Respectfully Submitted,

A handwritten signature in black ink, appearing to read "Sean McGinn", with a stylized flourish at the end.

Sean M. McGinn

Reg. No. 34,386

Date: April 19, 2002
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